



Attorney Docket No. 0756-2415

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hisashi OHTANI et al.

Serial No. 10/026,802

Filed: December 27, 2001

For: METHOD FOR MANUFACTURING A
SEMICONDUCTOR DEVICE

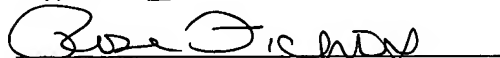
) Group Art Unit: J. Im

) Examiner: 2811

) CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

11-18-03



SUPPLEMENTAL RESPONSE

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

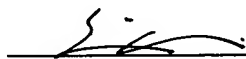
Sir:

This Supplemental Response is being filed to supplement the Response filed November 3, 2003 in response to the Official Action dated August 1, 2003.

The Official Action rejected claims 1-20 under the doctrine of obviousness-type double patenting over claims 1-26 of commonly owned U.S. Patent No. 6,335,541 to Ohtani et al. In response to this rejection, a *Terminal Disclaimer* is submitted herewith. Upon filing of this *Terminal Disclaimer*, the claims of the present invention are believed to be in condition for allowance. Reconsideration and withdrawal of the obviousness-type double patenting rejections are requested.

Should the Examiner believe that anything further would be desirable to place this application in better condition for allowance, the Examiner is invited to contact the Applicants' undersigned attorney at the telephone number listed below.

Respectfully submitted,


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